




ELECTRONIC INFORMATION DISCLOSURE STATEMENT

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Title of Invention	BIST CIRCUIT FOR MEASURING PATH DELAY IN AN IC						
Application Number: 10/628996							
Confirmation Number: 7336							
First Named Applicant: Olivier Ganry							
Attorney Docket Number: CRED 2617							
Art Unit: 2133							
Search string: (4875201 or 5923676 or 6058496).pn.							
US Patent Documents							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
1	1	4875201	1989-10-17	Dalzell			
2	2	5923676	1999-07-13	Sunter et al			
3	3	6058496	2000-05-02	Gillis et al			
Signature							
Examiner Name				Date			
N. Miska				07/31/2006			